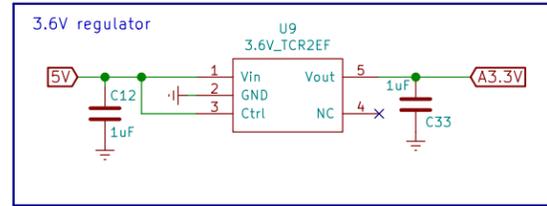
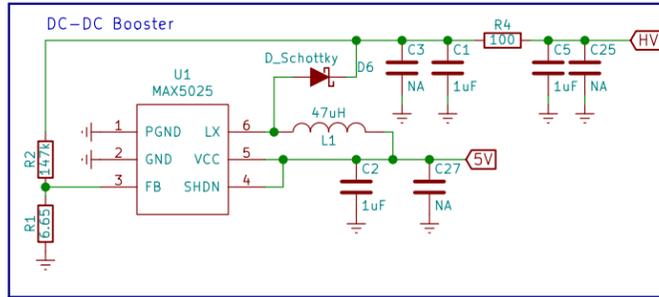
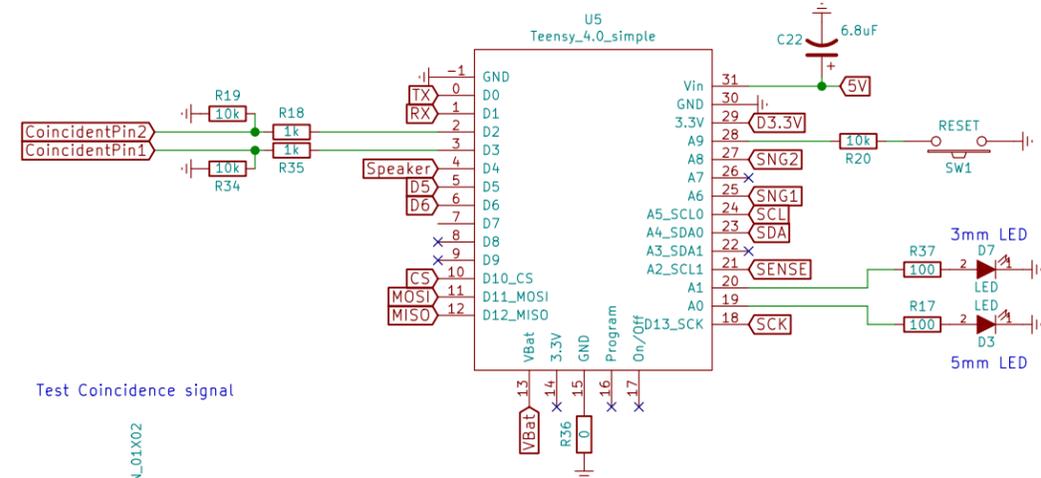
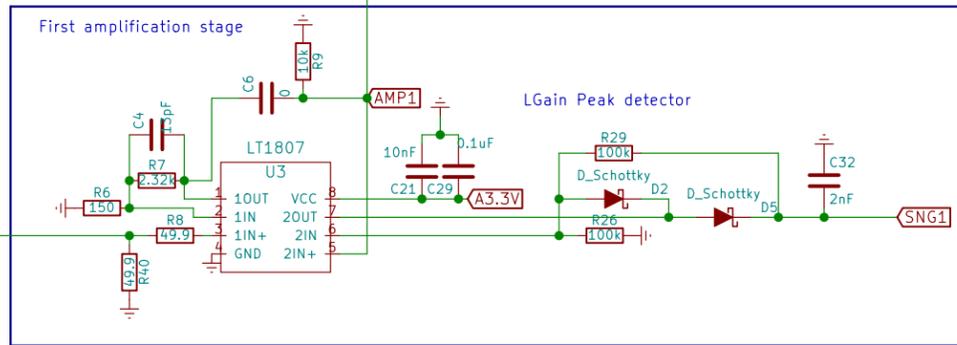
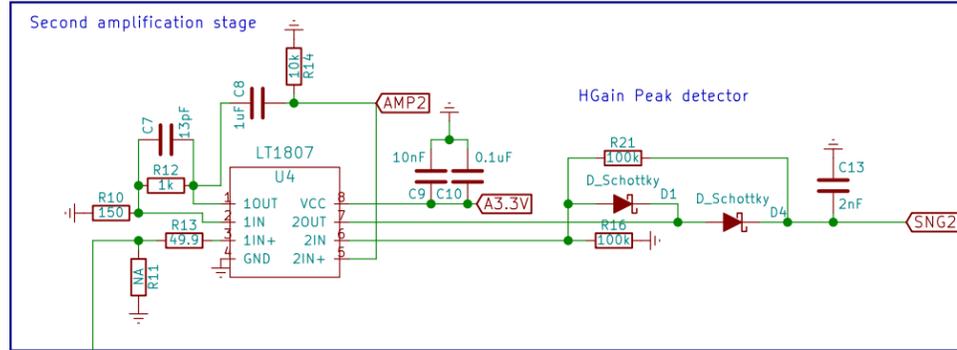
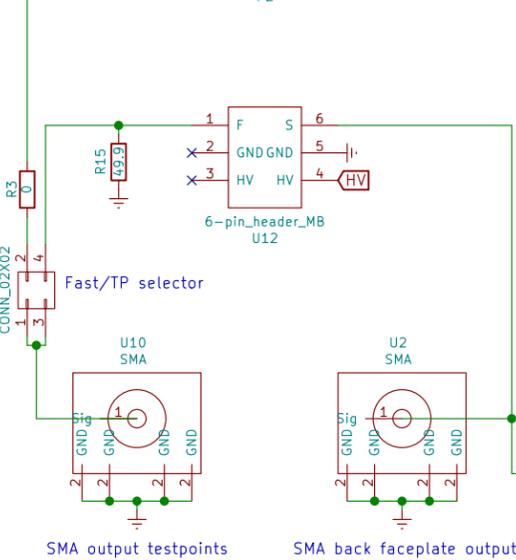
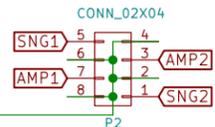


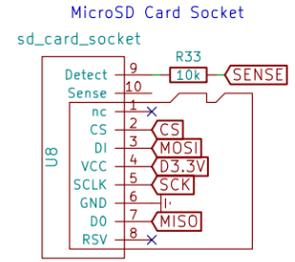
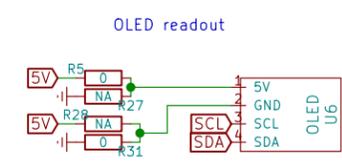
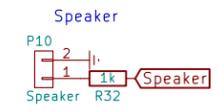
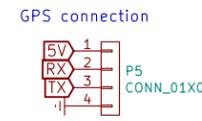
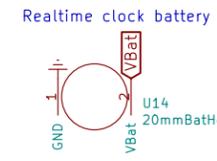
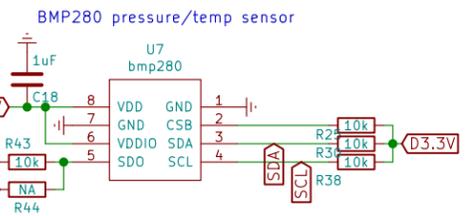
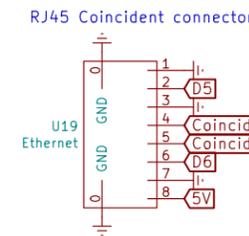
MainBoard



Testpoint selector



Test Coincidence signal



SiPM PCB

